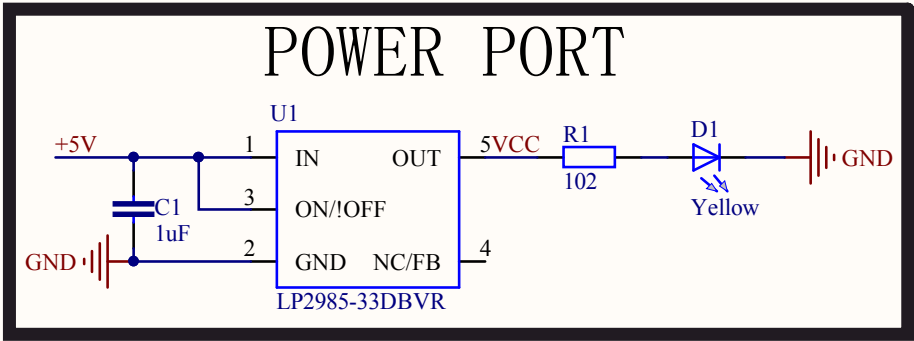


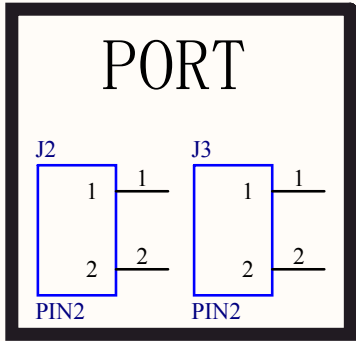
POWER PORT

The diagram illustrates a power port circuit. It features a +5V input line and a GND line. A 1uF capacitor (C1) is connected between the +5V line and GND. The input of the voltage regulator (U1, LP2985-33DBVR) is connected to the +5V line. The output of the regulator (OUT) is connected to a 102 resistor (R1), which is then connected to a yellow LED (D1). The LED is connected to GND. The regulator's ON/OFF pin is connected to GND, and its NC/FB pin is connected to the output. The output voltage is labeled 5VCC.

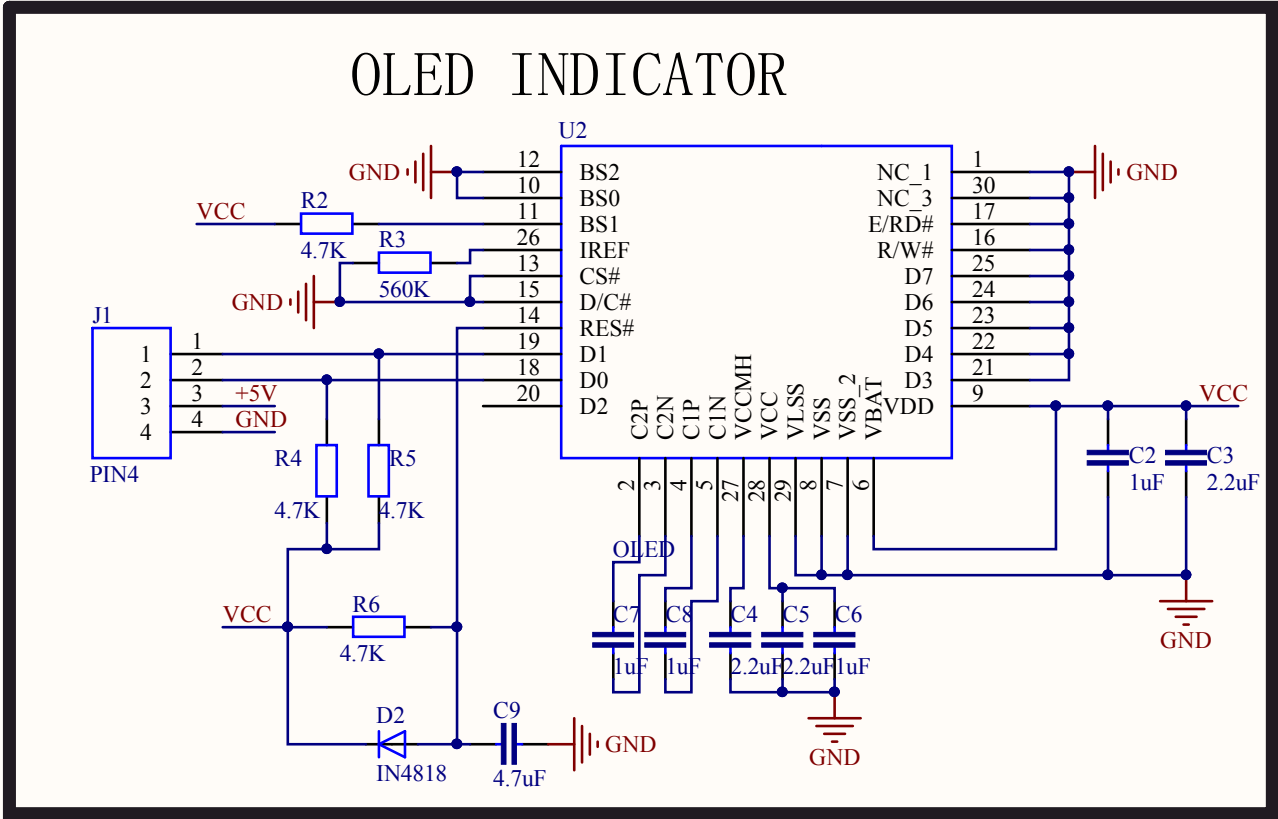


PORT

The diagram shows two ports, J2 and J3, each represented by a blue rectangle. Port J2 has two pins labeled 1 and 2, with the label PIN2 below it. Port J3 also has two pins labeled 1 and 2.



OLED INDICATOR



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